

ATM3202 Mini Module

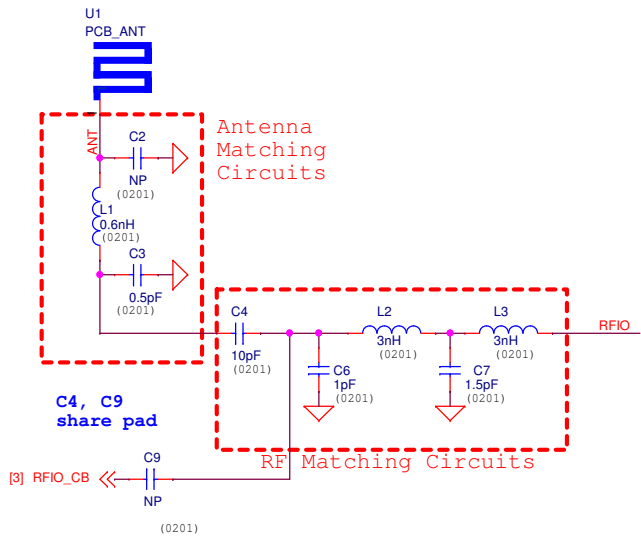
Release Date	Revision	Design Eng.	Design Note
2020/06/30	Rev. 0.1	Uma	Emigrate from ATM2202 Simple Module V02A
2020/08/28		Uma	Update matching network value of RFIO/WURX
2020/11/18		Uma	Update matching network value
2021/01/28		Uma	Update matching network value
2022/01/03	Rev. 1a	Joe	C8 (VAUX) changes from 1uF to 10uF
2022/05/23	Rev. 1b	Chris TsaiLin	Update matching for RF harvesting circuit - L10, L11 = 24nH - L8, L13 = 18nH - L7, L12 = 15nH - C16, C20 = 2pF

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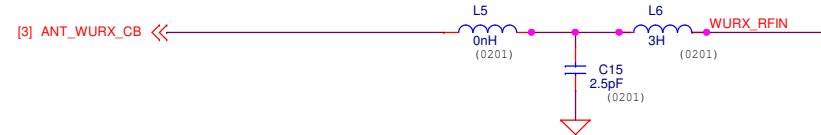
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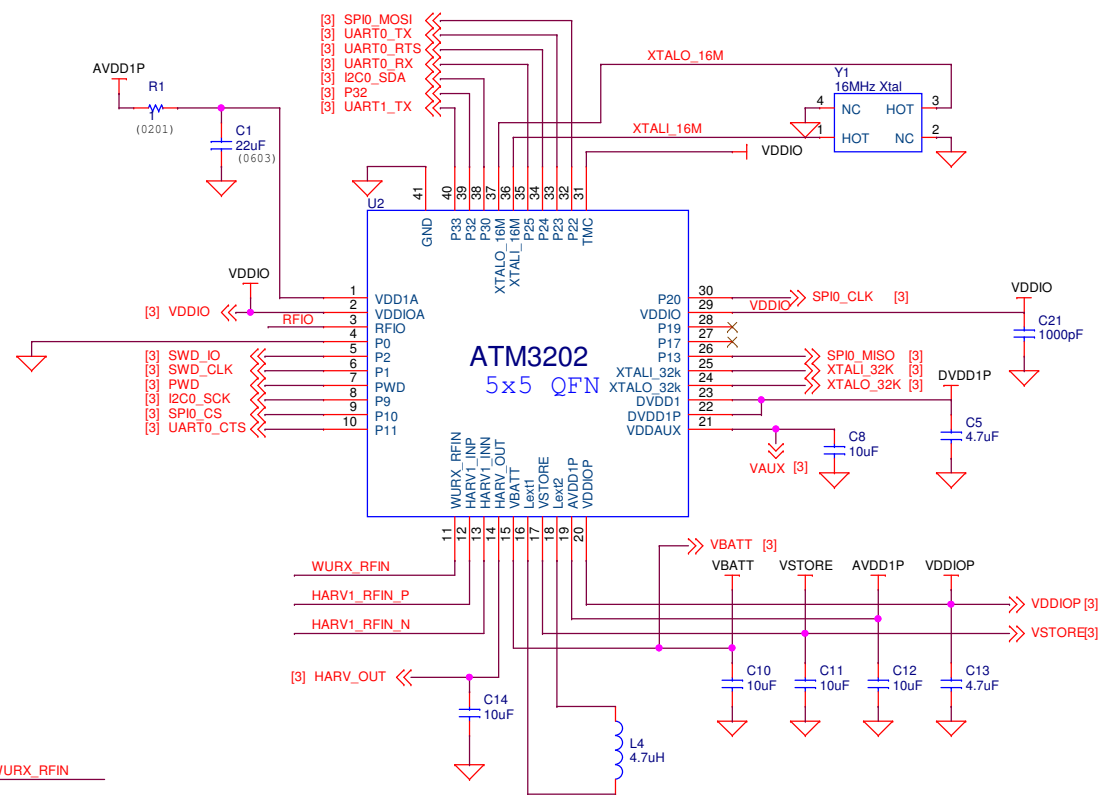
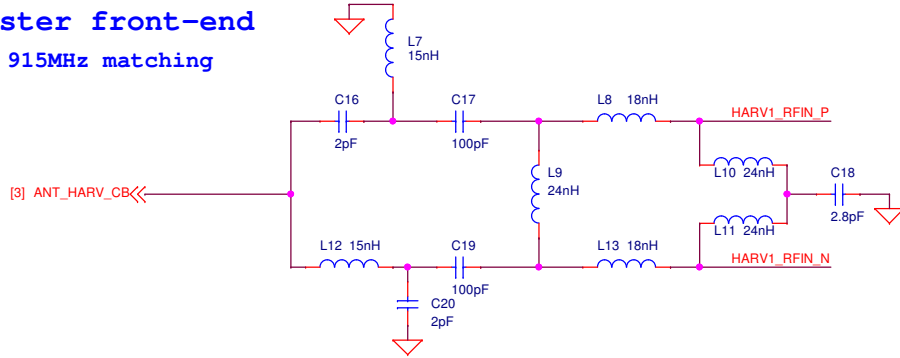
BLE front-end & ANT



WuRx front-end
Default 2.4GHz matching

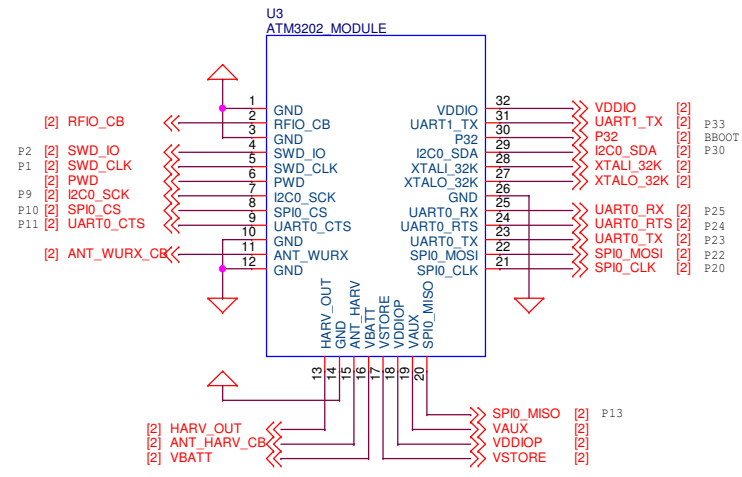


RF Harvester front-end
Default 915MHz matching



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Note:
Pin7-PWD and Pin22-P32 (BB00T) need to shunt
a 1M ohm resistor to ground on the carrier board.

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